Gianluca Brilli

Curriculum Vitae

PERSONAL DETAILS

Birth	April 24, 1991
Address	Via Statutaria 130 Casalgrande (RE)
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Skype	`Gianbrilli@hotmail.it`
Nationality	Italian

SHORT DESCRIPTION

Gianluca received his bachelor degree in Computer Science and master degree in Computer Engineering from University of Modena and Reggio Emilia. His main interests are numerical optimization, parallel computing and electronics, currently he is working on neural networks profiling on embedded low power platforms like FPGAs and GPUs.

EDUCATION

Master's Degree in Computer Engineering 2016-2018

University of Modena and Reggio Emilia

Subjects: Advanced Algorithms, Parallel Programming, Real-time Embedded Systems, Digital Electronic Design, Systems and Control Theory, Multimedia Elaboration Systems.

Thesis: Implementation and Validation of Convolutional Neural Networks on Embedded Automotive Platforms.

Advisor: prof. Marko Bertogna

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Bachelor's Degree in Computer Science 2012-2015

University of Modena and Reggio Emilia

Subjects: Algorithms and Data Structures, C/C++/Java/Python Programming, Numerical Analysis, Numerical Optimization, Database, Operating Systems, Calculator Architecture, Network Architecture and Protocols

Thesis: Metodi del Gradiente a Memoria Limitata per l'Ottimizzazione su Architetture Parallele.

Advisor: prof. Luca Zanni

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Diploma qualified as
"Perito Capotecnico in Elettronica e Telecomunicazioni" 2005-2010

ITIS A. Volta Sassuolo

Subjects: Electronics, Electrical engineering, Telecomunications, Control Theory

WORK EXPERIENCE

Internship in Real-Time Embedded Systems 2017-2018

Internship at High Performance Real-Time lab, belonging to Department of Physics, Informatics and Mathematics, in which I worked on implementation and validation of convolutional neural networks for objects detection and images classification on low power embedded platforms like FPGAs and GPUs. The most significant results were then summarized in two scientific articles [1] [2].

Internship in Numerical Optimization 2014-2015

Internship at Department of Physics, Informatics and Mathematics, in which I worked with my advisor prof. Luca Zanni, on parallelization of constrained optimization methods for image reconstruction like LMSD and BFGS on distributed memory architectures.

Progetti di Impresa srl 2011-2012

Work experience in which I worked as web programmer, in particular front-end developing for public administrations websites.

Microlog srl 2010-2010

Highschool stage in which I worked on electronic design and tesing of photoelectric systems for people counting.

SKILLS

Languages	Italian, English
Programming Languages	MATLAB, C/C++, Java, Python
Web	J2EE, Spring, Django, Jquery, AJAX
Embedded	Atmel and STM32 Microcontrollers, Xilinx FPGAs
Environments	Eclipse, ARM Keil, STMCube, Atmel Studio, Vivado, SDx
Parallel Programming	CUDA, OpenCL, OpenMP, MPI

PUBLICATIONS

[1] P. Burgio, G. Brilli, A. Marra, M. Bertogna, *Convolutional Neural Networks on embedded automotive platforms: a qualitative comparison*, preliminary version, Design, Automation and Test in Europe (DATE), workshop "W03: New Platforms for Future Cars (NPCar 2018)".

[2] P. Burgio, G. Brilli, A. Marra, M. Bertogna, *Convolutional Neural Networks on embedded automotive platforms: a qualitative comparison*, extended version, Advances in Parallel Programming Models and Frameworks for the Multi-/Many-core Era (APPMM 2018).

OTHERS

Organizing Committee member of the *International Workshop on Smart Cities and Future Eco-Systems (SCAFES 2018)*.

HOBBIES

Electronics and do it yourself,
Modeling,
Videogames,
Reading

Quanto dichiarato nel presente curriculum vitae corrisponde al vero ai sensi degli artt. 46 e 47 del D.P.R. 445/2000.

Sottoscrizione _____