

CURRICULUM VITAE - RUBEN ASANOVSKI

Personal info

Name: Ruben Asanovski
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Research experience

November 2020 – Present

PhD in ICT at the **University of Modena and Reggio Emilia** and in collaboration with **University of Udine**

Topic: “Alternative semiconductor materials and architectures for nanoelectronic devices”

Jan 2022 – Apr 2022

Visiting PhD student at imec (Belgium)

Sep 2022 – Dec 2022

Visiting PhD student at imec (Belgium)

February 2020 – October 2020

Internship at the **University of Modena and Reggio Emilia**, in collaboration with the **University of Udine** and the **Tyndall National Institute (Ireland)**.

Characterization of innovative devices through TCAD software and measurements on devices provided by European partners (Tyndall National Institute)

- TCAD simulation tool
- Presentation of scientific results in teleconferences
- Writing the results in scientific articles

Education

2018-2020

Master’s degree in Electronics Engineering 110/110 L
Università degli studi di Modena e Reggio Emilia

- **Thesis title:** “A new model of the low-frequency trapping/de-trapping noise in nanoscale MOSFETs”

2015-2018

Bachelor’s degree in Electronics Engineering 110/110 L
Università degli studi di Modena e Reggio Emilia

- **Thesis title:** “Analisi di rumore negli stadi di amplificazione elementari”

2015

High school diploma: Elettronica ed Automazione **100/100 L**
ITI Enrico Fermi, Modena (MO)

Programming languages

C, VHDL, Assembly

Language knowledge

Italian	Mother tongue
English	B2 certified
Spanish	A2
Macedonian	Mother tongue

University projects

Smart Controller for automotive use in collaboration with Ferrari GES **2019**
Design of an "automotive compliant" system for monitoring the pressure and vibration of a DRS wing of an F1 car.

Design of a non-invasive pulse oximeter **2018**
Design of a measurement system for the estimation of blood oxygenation and heart rate: from front-end electronics to data processing in Arduino/LabView environment.

Design of a Two-Stage OTA **2018**
Design of a Two-Stage OTA from the specifications to the circuit realization on a simulation environment of IC (Cadence Virtuoso).

Design of a 2018 3.1 GHz patch antenna **2018**
Design and implementation of a patch antenna on printed circuit board working at 3.1 GHz through a microwave simulation environment (CST Studio Design).

Step length estimation algorithm **2018**
Implementation of an algorithm to estimate the length of the step length of a person through the processing on MatLab of data obtained from a wearable IMU.

Door opening system with password **2017**
Realization of a door opening system by entering a password implemented with a microcontroller and several sensors.

Awards

Dr.ssa Ing. Lorena MORLINI - in memory" Study Award - 1st edition **2018**

Best paper award at EUROSOI-ULIS 2022 **2022**

Best student paper award at IEDM 2022 **2022**